INFORMATION HANDLING SYSTEM HAVING INTEGRATED INTERNAL SCALABLE STORAGE SYSTEM

ABSTRACT

An information handling system having an integrated internal scalable switching storage system is disclosed. The information handling system includes a housing, a processor disposed in the housing and a memory device for storing a program of instructions executable by the processor. A bus of the information handling system is disposed in the housing, and the processor and the memory are coupled to the bus. A host bus adapter is coupled to the bus and is disposed in the housing. An information storage system is in turn coupled to the host bus adapter and is also disposed in the housing. The information storage system includes a backplane for coupling at least one information storage device to the host bus adapter. The information storage system and host bus adapter are preferably compliant with a scalable, switching, mass storage system standard such as Fibre Channel.